

Reference Specification

High Voltage Ceramic Capacitor DHR1X4D200K1HB Issued Date: November 8, 2011

Product specifications in this drawing are subject to change or our products described in this drawing may be discontinued without advance notice. The parts numbers and specifications listed in this drawing are for information only. You are requested to transact the "Approval Sheet Product Specification", before your ordering.

ENGINEERING SECTION CAPACITOR DIV.2

IZUMO MURATA MANUFACTURING. Co., LTD

▲ CAUTION

1. OPERATING VOLTAGE

When DC- rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.



©Transportation equipment(vehicles, trains, ships, etc.)

®Disaster prevention / crime prevention equipment

⑦Traffic signal equipment
 ⑨Data-processing equipment

[®]Application of similar complexity and/or reliability requirements to the applications listed in the above.

NOTICE

Cleaning To perform ultrasonic cleaning, observe the following conditions. Rinse bath capacity : Output of 20 watts per liter or less. Rinsing time : 5min maximum. Do not vibrate the PCB/PWB directly. Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

Capacitance change of capacitor

Class 1 capacitors

Capacitance might change a little depending on a surrounding temperature or an applied voltage. Please contact us if you use for the strict time constant circuit.

Class 2 and 3 capacitors

Class 2 and 3 capacitors like temperature characteristic B, E and F have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time.. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit. Please contact us if you need a detail information.

▲ NOTE

- 1. Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from the agreed specifications.
- 3. We consider it not appropriate to include any terms and conditions with regard to the business transaction in the product specifications, drawings or other technical documents. Therefore, if your technical documents as above include such terms and conditions such as warranty clause, product liability clause, or intellectual property infringement liability clause, they will be deemed to be invalid.

	ng temperatur to +85°C						
2.2 Part un <u>DHR</u> Series	mber <u>1X</u> Temperature characteristic	<u>4D</u> Rated voltage	<u>200</u> Capacitance	<u>K</u> Capacitance tolerance	<u>1H</u> Lead code	<u>B</u> Packing style code	Individual specification
• Temp	erature charad	cteristic					
-		Temperatu		eristic			
	1X		SL				
	Please c	onfirm det	ailed specific	ation on [5. S	Specificati	on and test r	nethods].
• Pato	dvoltago						
• Rale	d voltage Code	Ra	ted voltage				
	4D		DC20kV				
·			rt unmber list	:].			
 Lead 							
	Code		ead style				
	1H Blogge ref		raight long art unmber lis				
	Flease let	1 IU [4. Fo		5t J.			
S	Solder coated o	copper wire	e is applied fo	or termination			
• Dook	ing code						
• Pack	Code		acking type				
• Pack			Bulk type				
• Pack	В						
• Indivi In c	dual specificat		be identified	l without 'indi [,]	vidual spe	ecification', it	is added at the end
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5.TE	ST	
5-1	VISUAL EXAMINATION CHECK	OF DIMENSIONS

			OF DIMENSIONS	
No.	ITEM		SPECIFICATION	TESTING METHOD
1	Appearance dim	ensions	See 3,4.	Shall be visually examined or Venire
				Calipers.
2	Marking		To be easily legible.	Shall be visually examined.
5-21	ELECTRICAL PE	RFORMANCE T	ESTS	
1	Dielectric	Between	No failure	The capacitors shall not be damage
	Strength	Lead wires		when DC voltage of 150% of the rated
				voltage are applied between the lead
				wires for 60 s in insulate liquid or gas.
				(Charge/discharge current: 50mA max.)
		Body		The capacitors is placed in the
		insulation		container with metal balls of diameter
				1mm so that each lead wires,
				Short-circuited, is kept approximately
				2mm off the balls as shown in the
				figure, and DC voltage of 3kV is
				applied for 10 s between capacitor
				lead wires and small metals.
				(Charge/discharge current: 50mA max.)
2	Insulation	Between	10,000MΩ min.	The insulation resistance shall be
	Resistance	Lead wires		measured with DC 1kV within 60±5 s of
	(I.R.)			charging.
3	Capacitance		Within the specified tolerance.	The capacitance shall be measured at
				20°C with 1±0.2kHz and AC5V(r.m.s.)
				max
4	Dissipation Fa	ctor	1.0% max.	Same condition as capacitance.
	(D.F.)			
5	5 Temperature Characteristic		-350 ~ -1000ppm/°C	The capacitance measurement shall be
				made at each step specified in table.
				Capacitance change from the value of step
				3 shall not exceed the limit specified.
		1		SL 20±2°C 85±2°C 20±2°C
6	Charge	Appearance	No marked defect.	Charge discharge test shall be measured
	Discharge	Capacitance	Within ±10%	in the following test circuit and cycle.
	Test	Change		Applied voltage: rated voltage
		D.F.	1.5% max.	Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored
		I.R.	5,000MΩ min.	for 4 h at room condition.
		Dielectric	No failure	
		Strength		6 s E:Direct-current
		(Between		► 1 < 2 1 < 2 (s) Voltage source
		lead wires)		for Cx.
				R1, M for Cx. (Co≠10Cx)
				\mathbf{F}_{E} = \mathbf{F}_{E}
				Co R2 Resistor (Ε/10Ω)
7	Corona Test	I	Corona shall be 50 pico	Corona shall be measured in the
'				following test circuit.
			reading.	Applied voltage: AC5.0kV(r.m.s.)
				Applied Voltage. A00.0KV(1.11.5.)
				R E:power supply (corona free)
1 I				Capacitor LILM Cx:speciman
				L:corona pulse pick-up coil

5-3 MECHANICAL PERFORMANCE TESTS

	IECHANICAL PE			
No.	ITE		SPECIFICATION	TESTING METHOD
1	Strength	Pull	Lead wire shall not cut off.	Fix the body of capacitor, apply a
	of Lead			tensile weight gradually to each lead
			broken.	wire in the radial direction of capacitor
				up to 10N, and keep it for 10±1 s.
		Bending		Each lead wire shall be subjected to 5N
		-		weight and then a 90° to bend, at the point
				of egress, in one direction, return to
				original position, and then a 90° bend in
				the opposite direction at the rate of one
				bend in 2 to 3 s.
2	Solderability of L	eads		The lead wire shall be dipped into a
			with uniformly coated on the	25% methanol solution of rosin and
				then into molten solder of 235±5°C for
			circumferential direction.	2±0.5 s.
				In both cases the depth of dipping is up to about 1.5 to 2.0mm from the root of
				lead wires.
3	Soldering	Appearance	No marked defect.	The lead wires shall be immersed into
3	Effect			the melted solder of 350±10°C up to
1		Capacitance	Within±10%	about 1.5 to 2.0mm from the main body
		Change Dielectric	No failure	for 3.5±0.5 s.
		Strength	No fallure	Post-treatment: Capacitor shall be stored
		(Between		for 24 ± 2 h at room condition.
		lead wires)		
5-4 (LIMATIC TESTS			
No.	ITE	NA	SPECIFICATION	TESTING METHOD
110.	Humidity	Appearance	No marked defect.	Set the capacitor for 240±8 h at 40±2°C
	(under steady	Capacitance	Within±10%	in 90 to 95% humidity.
	state)	Change		Post-treatment: Capacitor shall be stored
		D.F.	1.5% max.	for 1 to 2 h at room condition.
		I.R.	5000MΩ min.	(Charge/discharge current: 50mA max.)
		Dielectric	No failure	(***);*****;***************************
		Strength		
		(Between		
		lead wires)		
2	Life	Appearance	No marked defect.	Apply a DC voltage of 125% of the
	(high temperature		Within±10%	rated voltage for 1000+48/-0 h in silicon
	load)	Change		oil at 85±2°C.
		D.F.	1.5% max.	Post-treatment: Capacitor shall be stored
		I.R.	5000MΩ min.	for 24±2 h at room condition.
		Dielectric	No failure	(Charge/discharge current:50mA max.)
		Strength		
		(Between		
		lead wires)		
3	Temperature	Appearance	No marked defect.	+85 °C
	Cycling	Capacitance	Within±10%	
1		Change		
		D.F.	1.5% max.	20%0
		I.R.	5000MΩ min.	-30°C
		Dielectric	No failure	
		Strength		Temperature cycling shall be measured
		(Between		in the following test. Cycle numbers: 5 cycles
		lead wires)		Post-treatment: Capacitor shall be stored
		,		for 4 h at room condition.
				IUI 4 II al IUUIII CUIUIIIUII.

Note) Tests for Dielectric strength ,Charging/Discharging test, Humidity , Life and Temperature cycling shall be performed with specimens having molded resin (MR1023C:made by Murata) extending over 3mm on all the surface.

Room condition: Temperature:15~35°C Humidity:45~75% Atmospheric pressure:86~106kPa

